

LOW OUTPUT LOAD  
DETECTION CIRCUIT TO  
SIGNAL IDLE COMMAND  
SHEET 3 OF 3

FIG. 5  
M20

Drive\_A

FIG. 5  
M24

Drive\_B

R144 10K

D16 1N5240B 10V

R143 100K

R32 100K

R31 100K

R21 100K

CW

FIG. 16  
U16D-14

R35 1.00K

R34 22.1K

D48 1N5240B

FIG. 17

U8C 14

CONT IN OUT

U8D 14

CONT IN OUT

U14C LM339

Vbr 12

V- 8

V+ 9

14

LOGIC LOW = IDLE MODE

R30 10K

C12 1 uF

C14 1uF 15V

D37 FIG. 12 U14B-1

D50 40.2K FIG. 8 U16C-10

D10 FIG. 8 U16C-10

D35 FIG. 7 R76 U16C-9

D51 FIG. 4 U2-4

FIG. 8 U8A-13

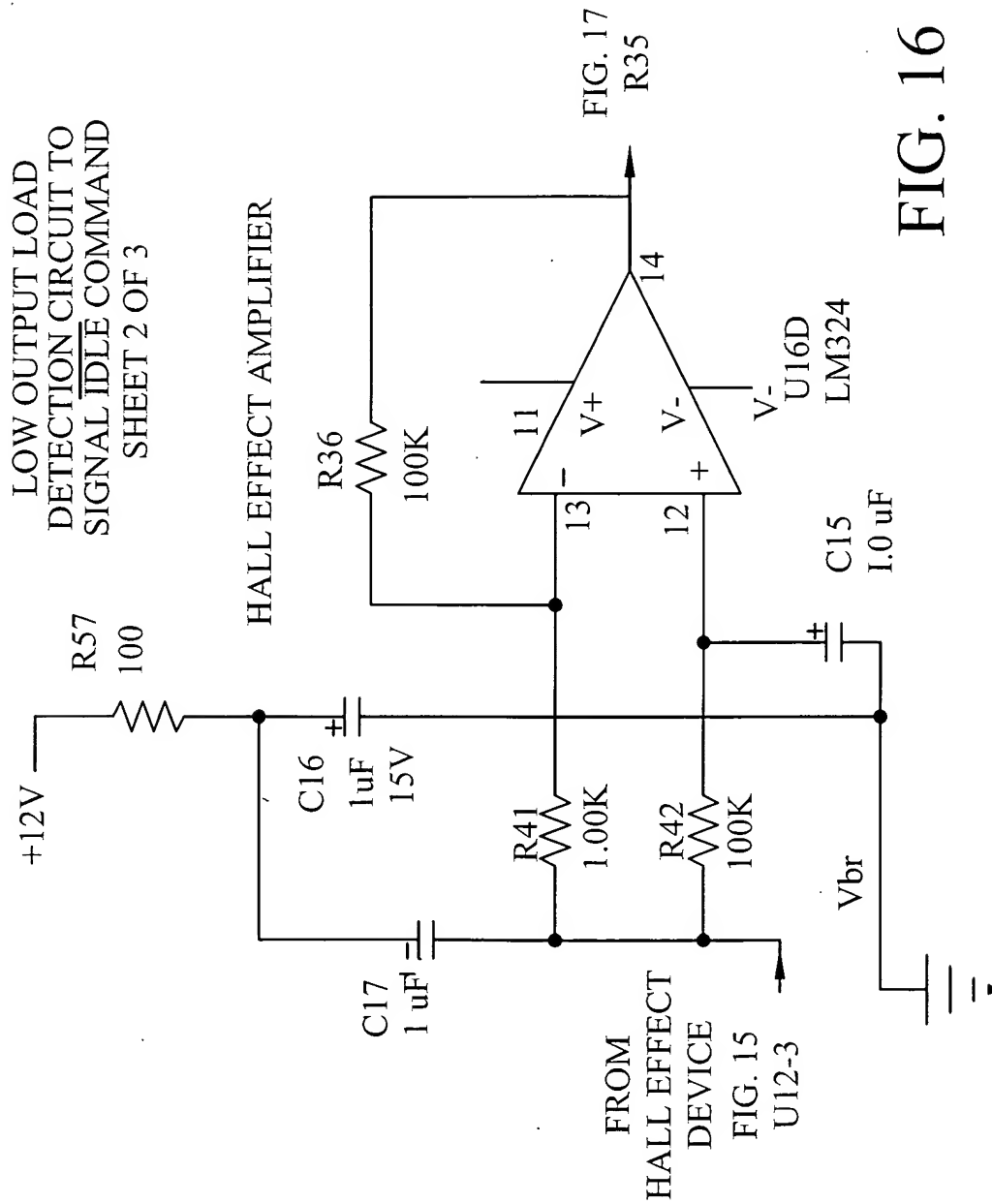
R142 100 M

C13 1uF

Vbr

VSS

FIG. 17







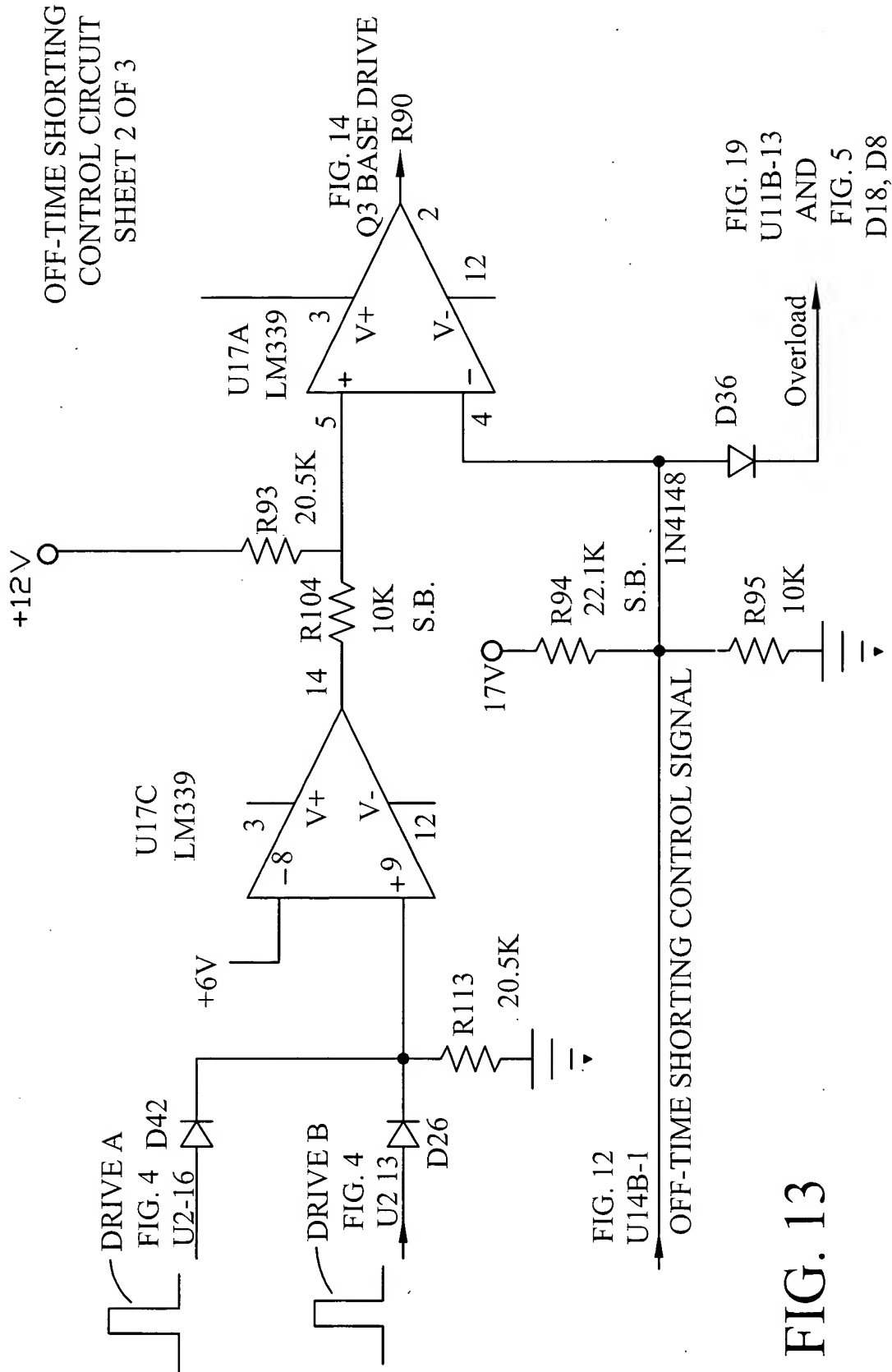


FIG. 13

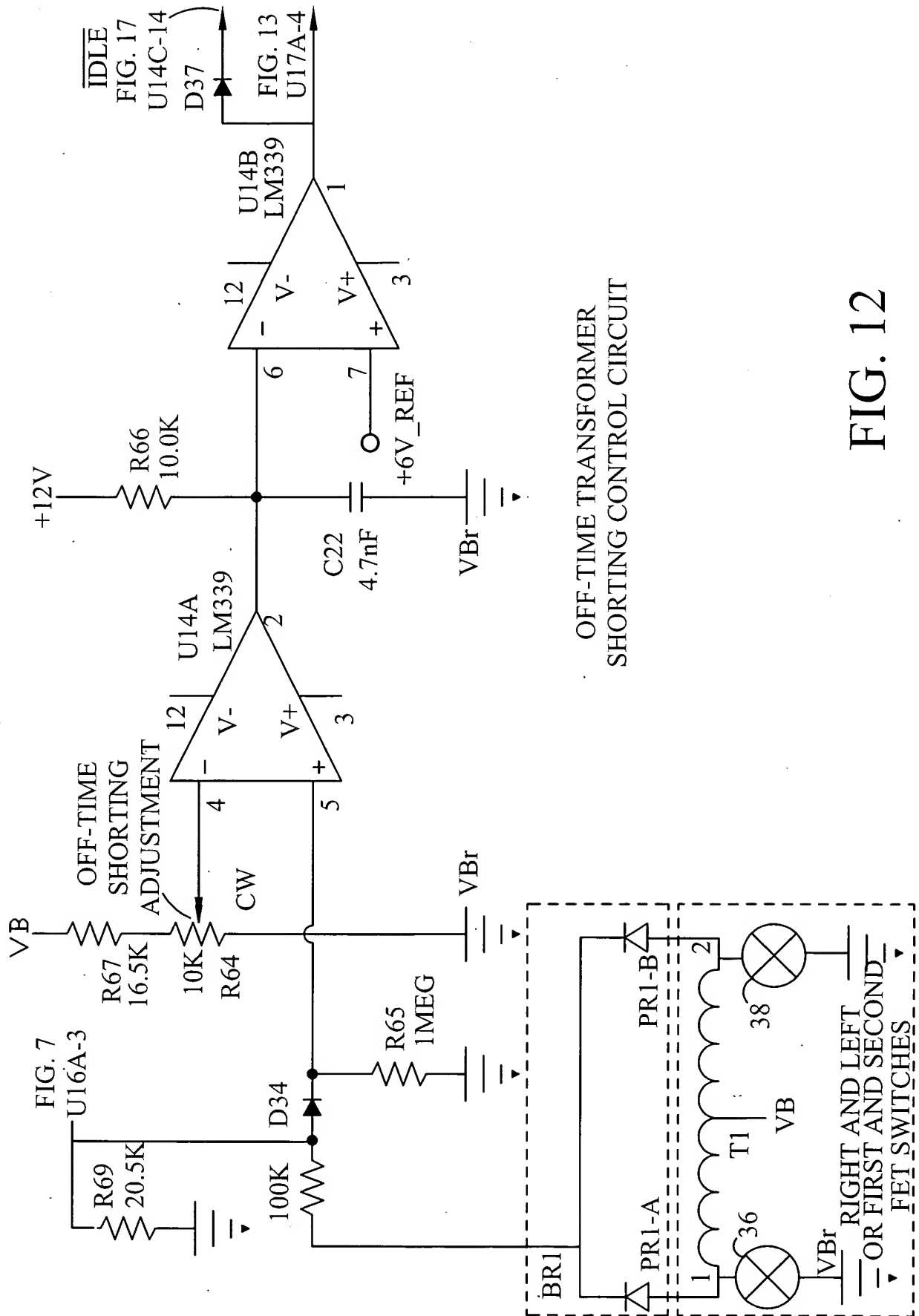


FIG. 12

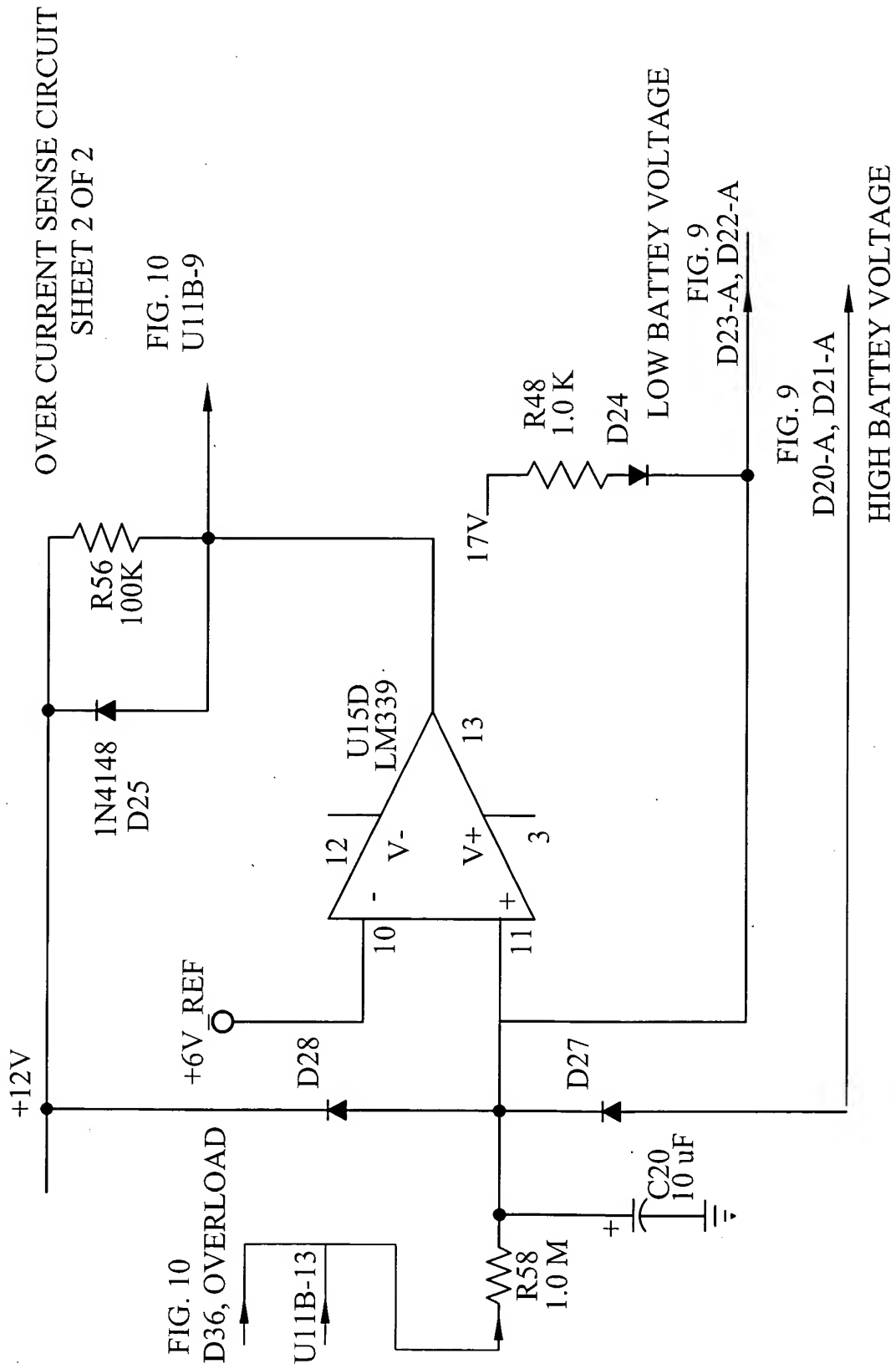
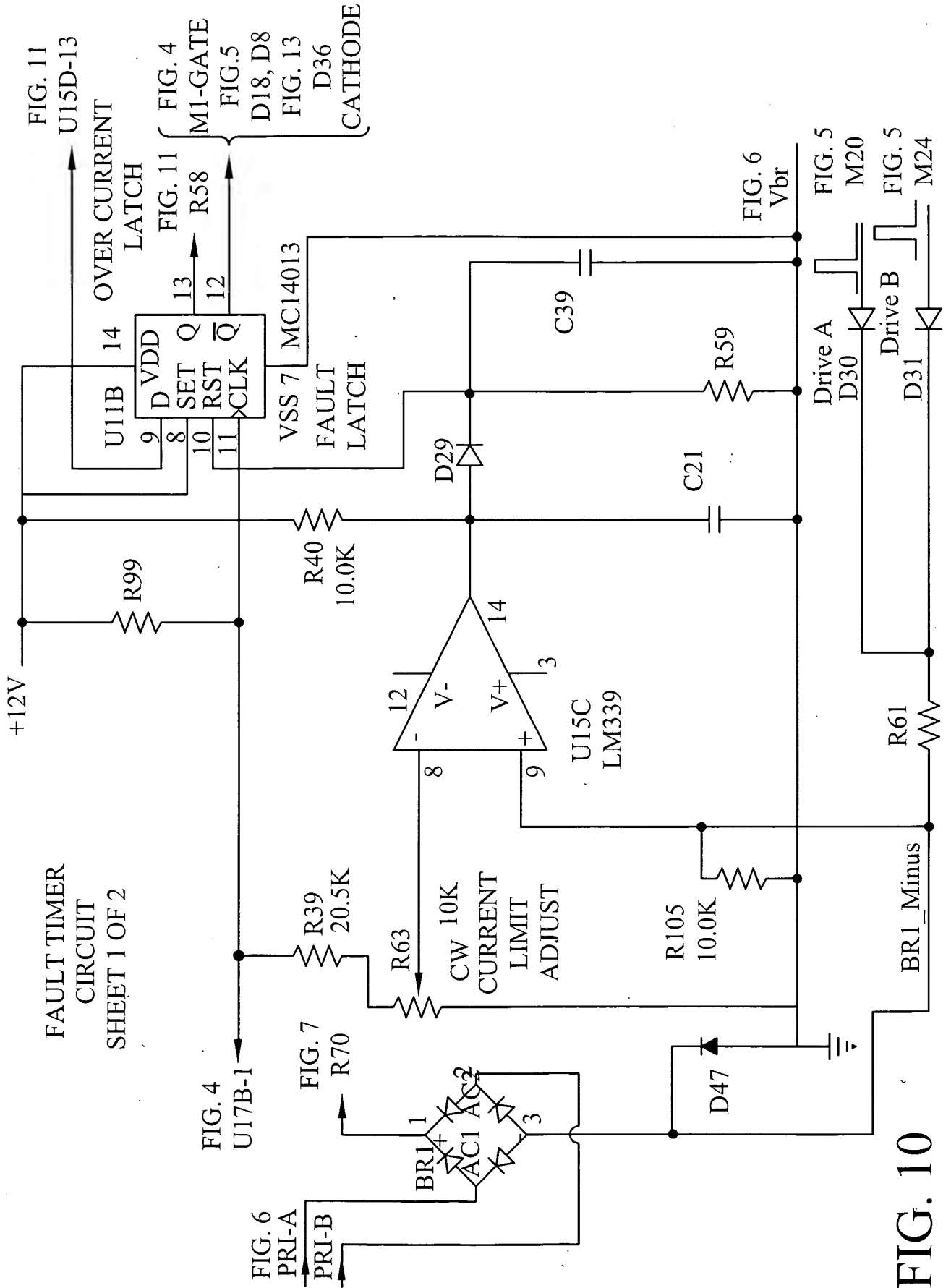


FIG. 11





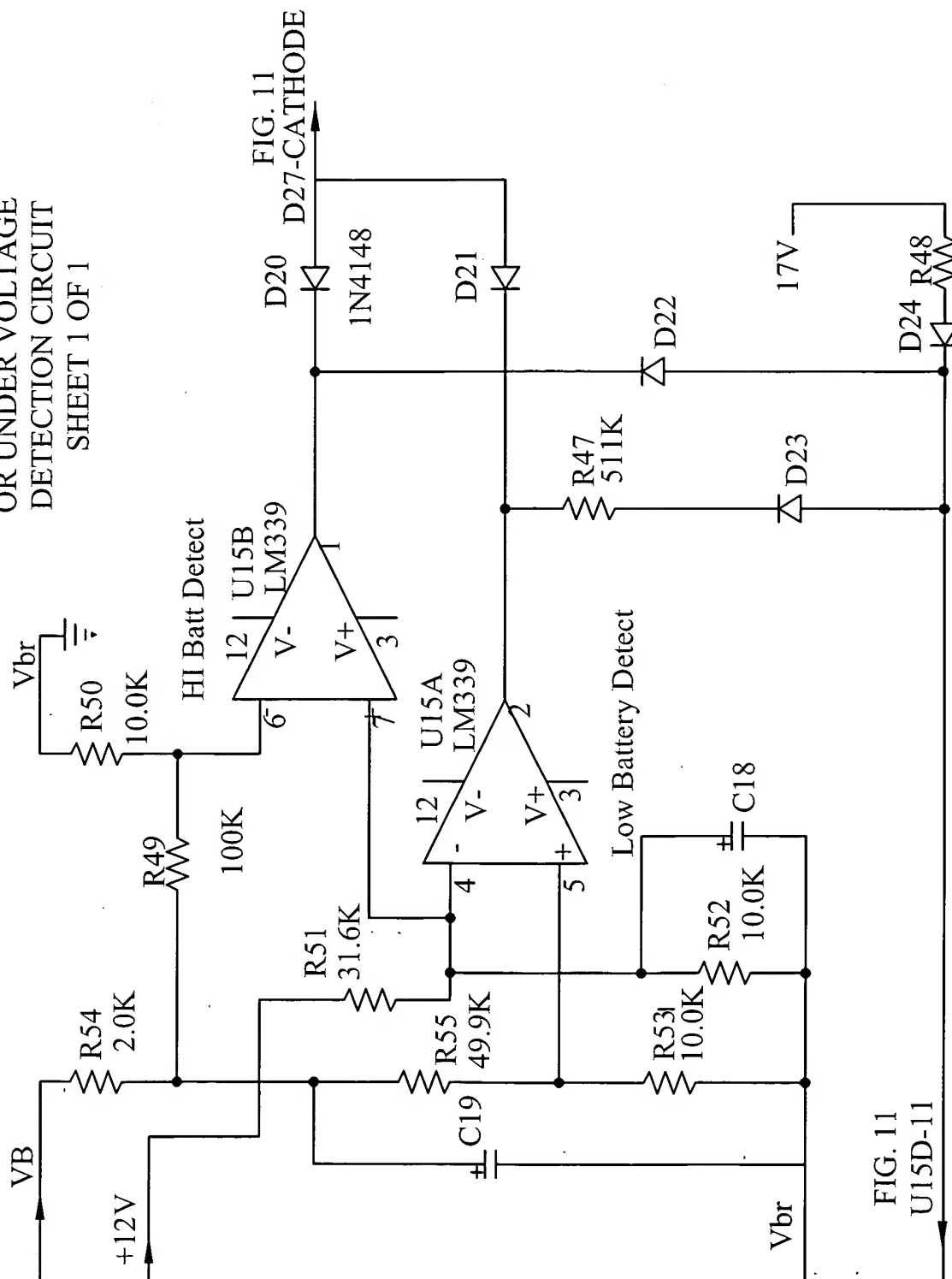
FIG. 11  
U15D-11

FIG. 9



FIG. 8

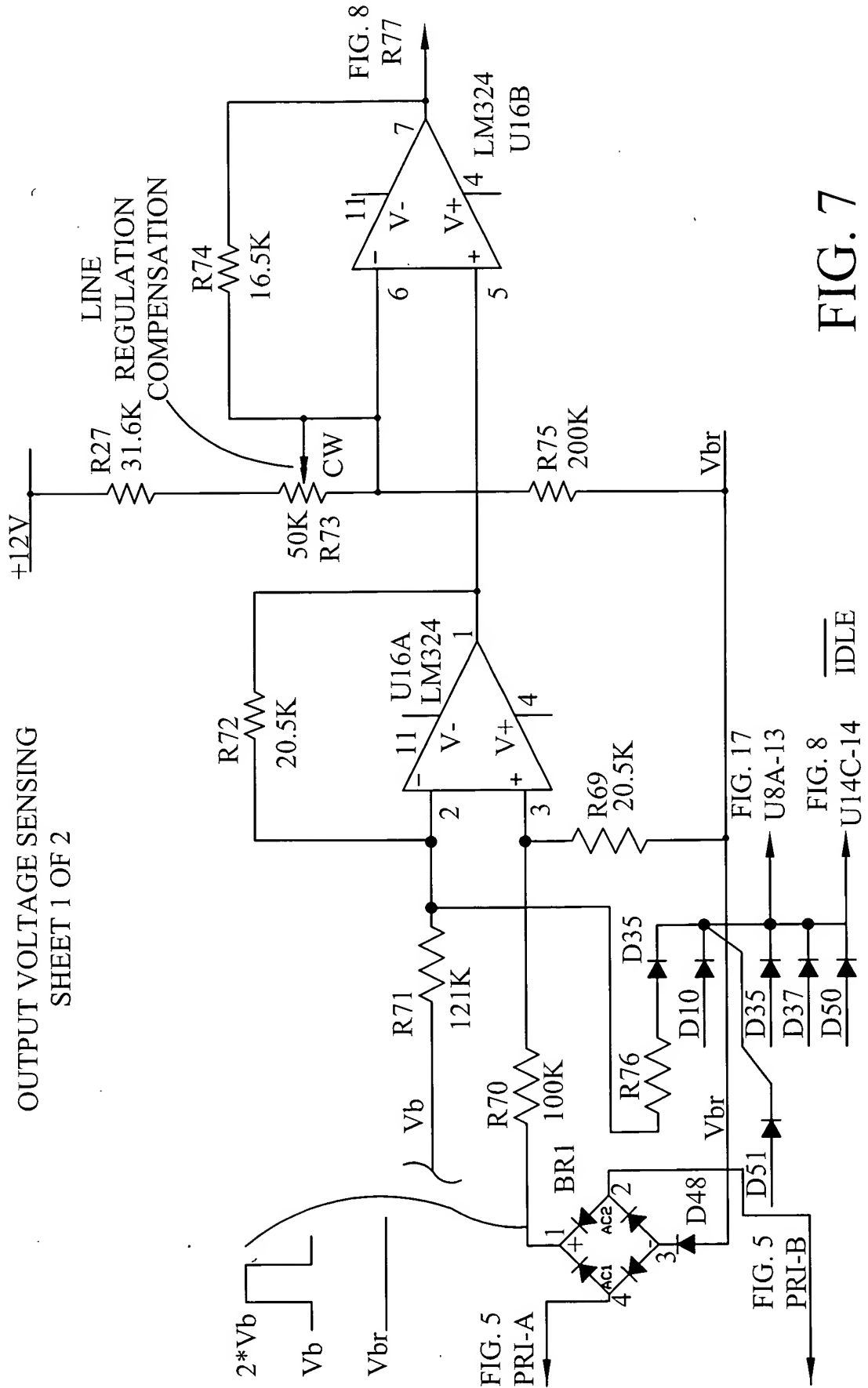


FIG. 7

OUTPUT VOLTAGE SENSING  
 SHEET 1 OF 2

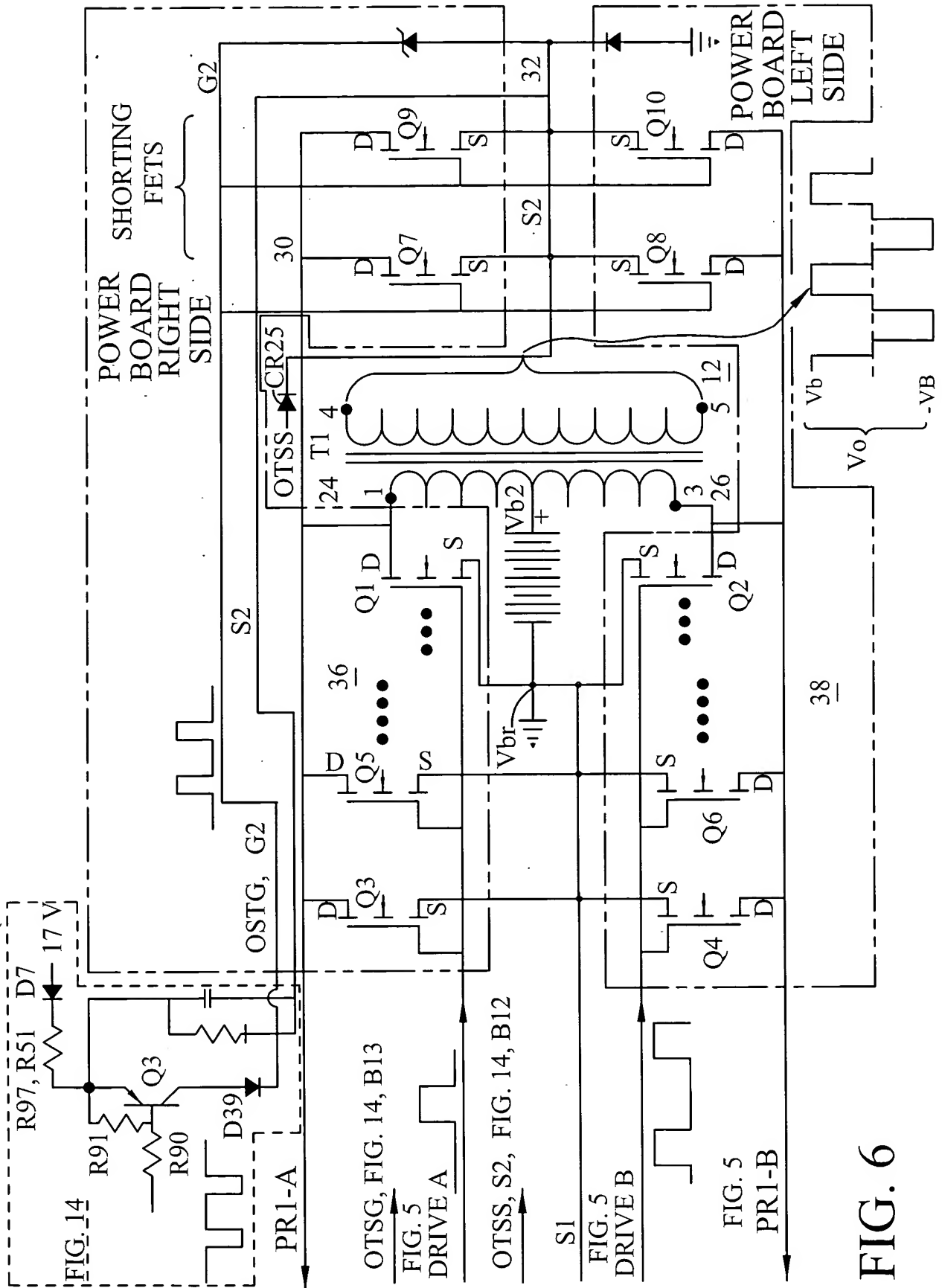


FIG. 5



FIG. 4

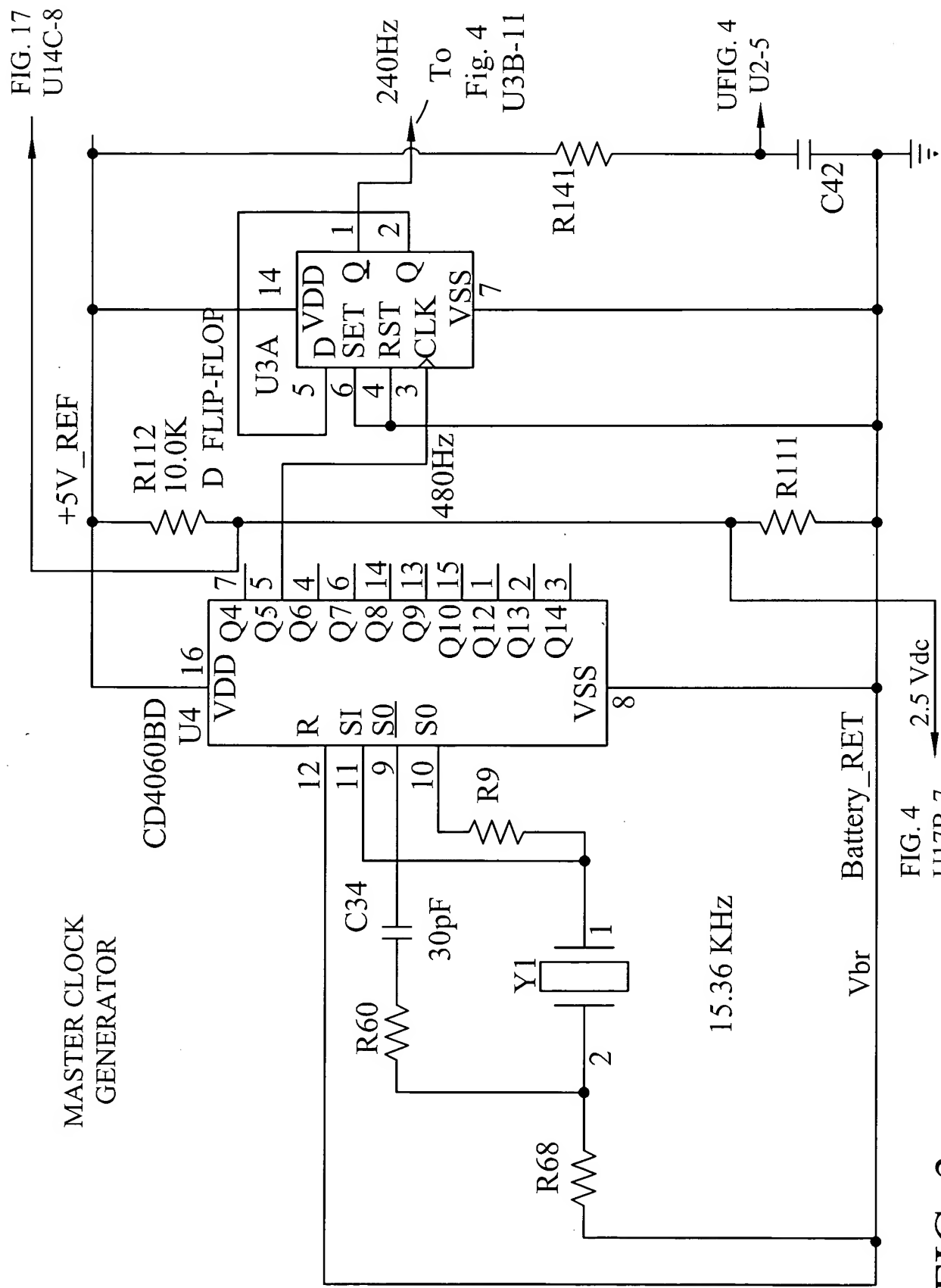


FIG. 4  
U17B-7

FIG. 3

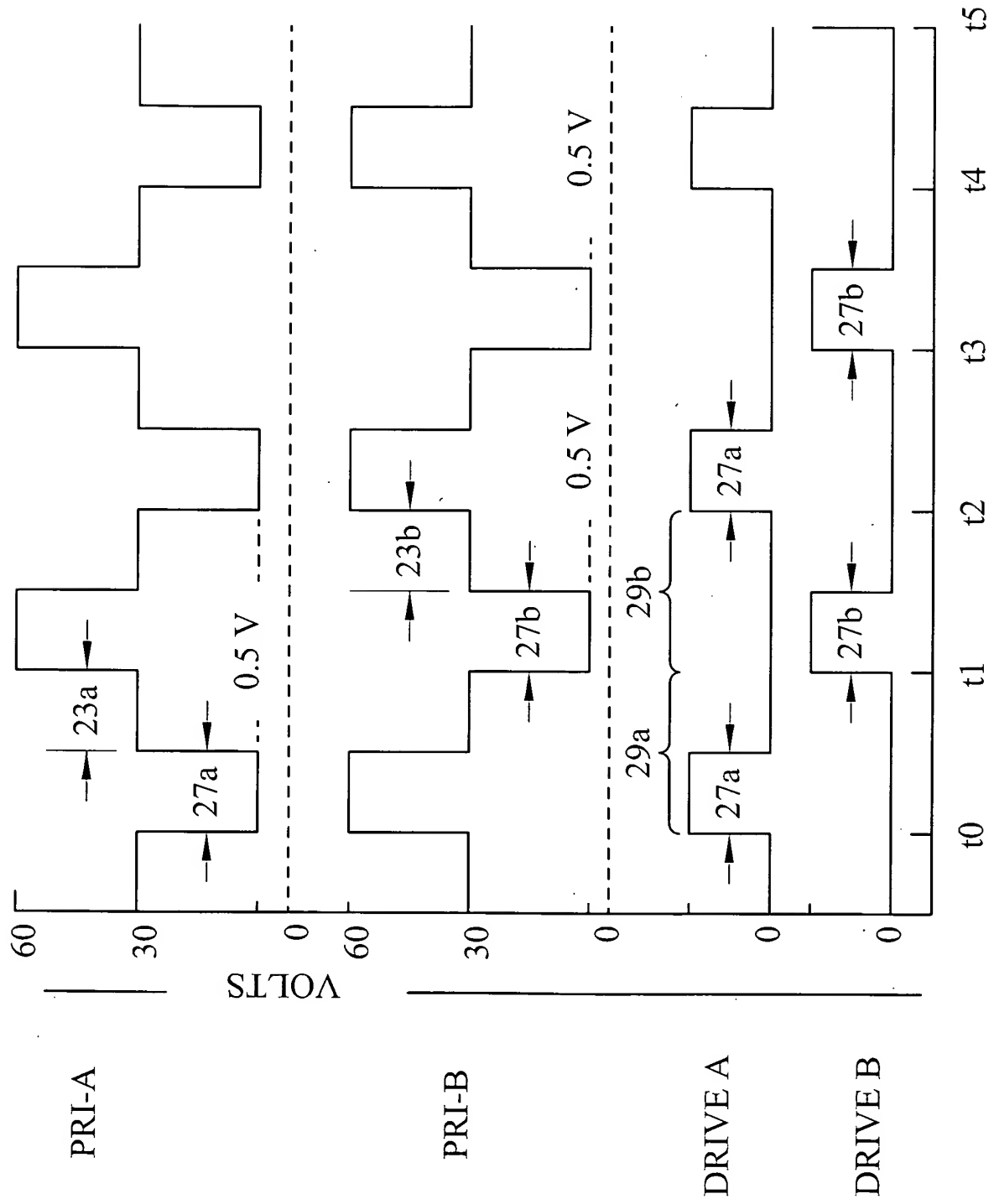


FIG. 2



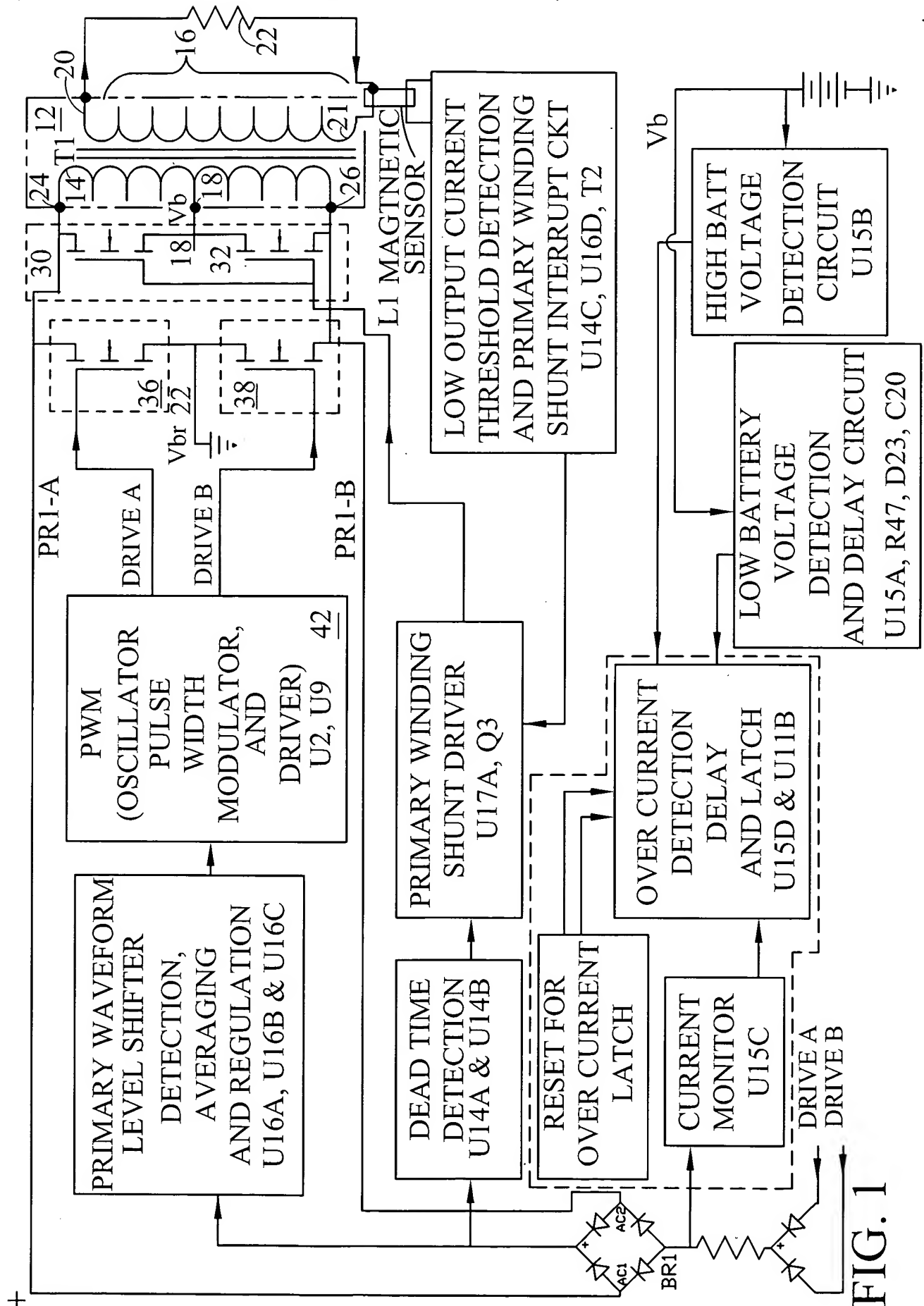


FIG. 1